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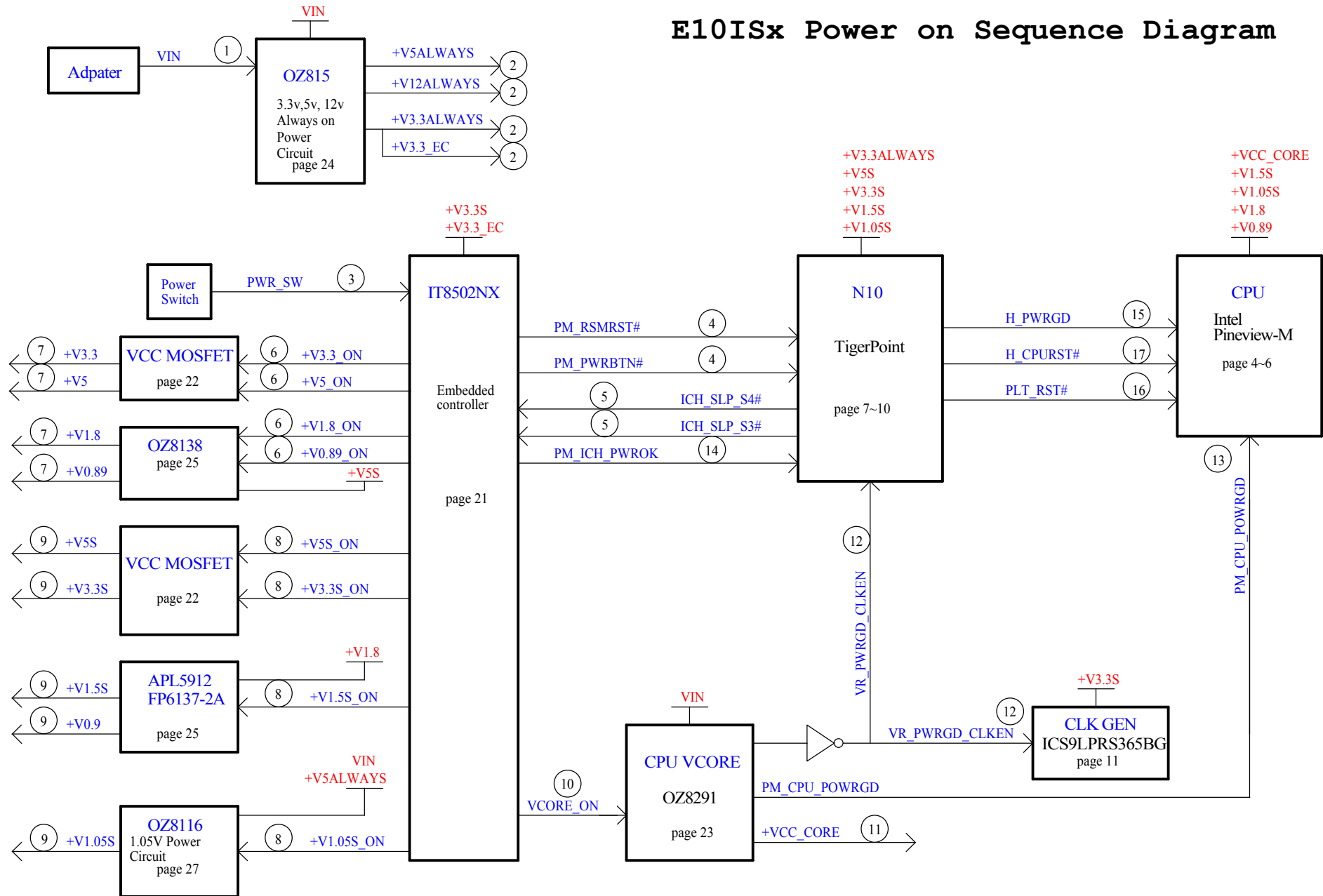
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| 24. | +CPU_CORE (OZ8291) |
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Power Rail

+VCC_CORE	Core voltage for Processor(off in S3-S5)
+V1.05S	1.05V switched power rail (off in S3-S5)
+V1.8	1.8V power rail (off in S4-S5)
+V0.9S	0.9V switched power rail(off in S3-S5)
+V1.5S	1.5V switched power rail (off in S3-S5)
+V0.89S	0.89V switched power rail (off in S3-S5)
+V1.8S	1.8V switched power rail (off in S4-S5)
+V5	5V power rail (off in S4-S5)
+V5S	5V switched power rail (off in S3-S5)
+V5ALWAYS	5V always on power rail
+V3.3ALWAYS	3.3V always on power rail
+V3.3S	3.3V switched power rail (off in S3-S5)
+V3.3	3.3V power rail (off in S4-S5)

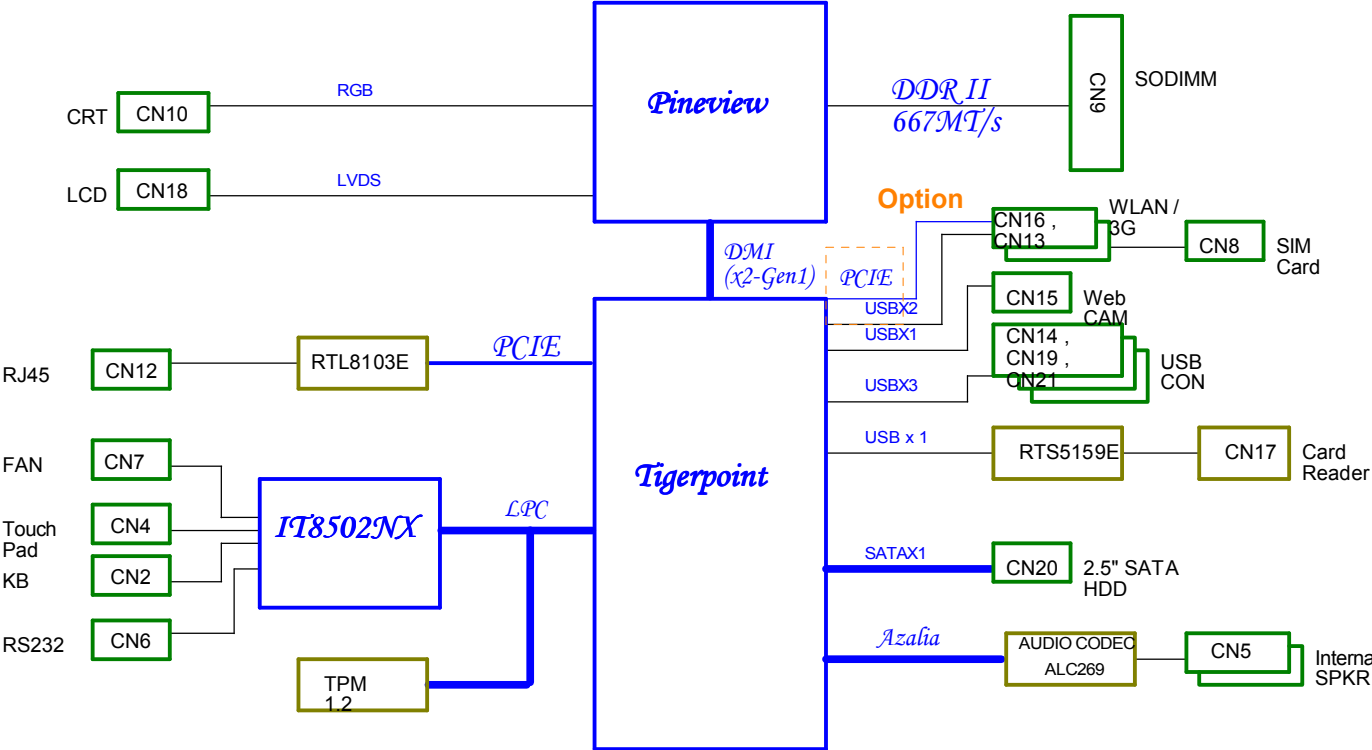
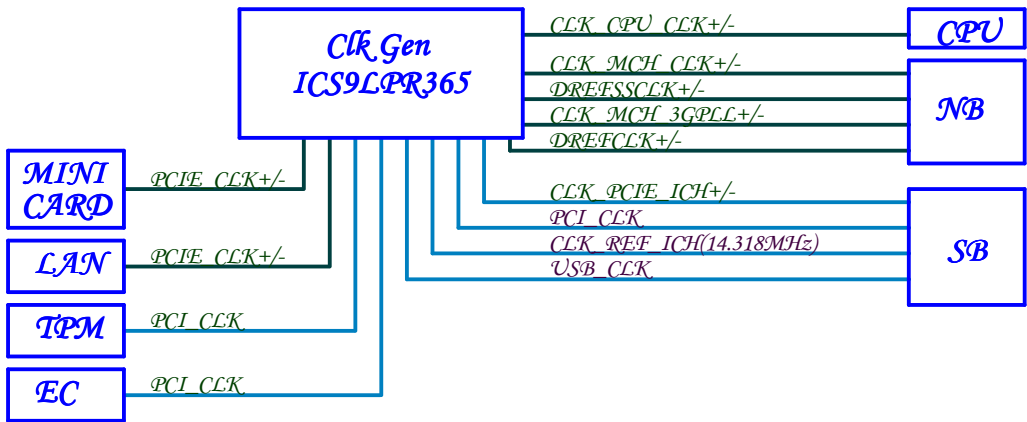
+V□□ALWAYS	Always on power rail
+V□□	Switched power rail (off in S4 - S5)
+V□□S	Switched power rail (off in S3 - S5)

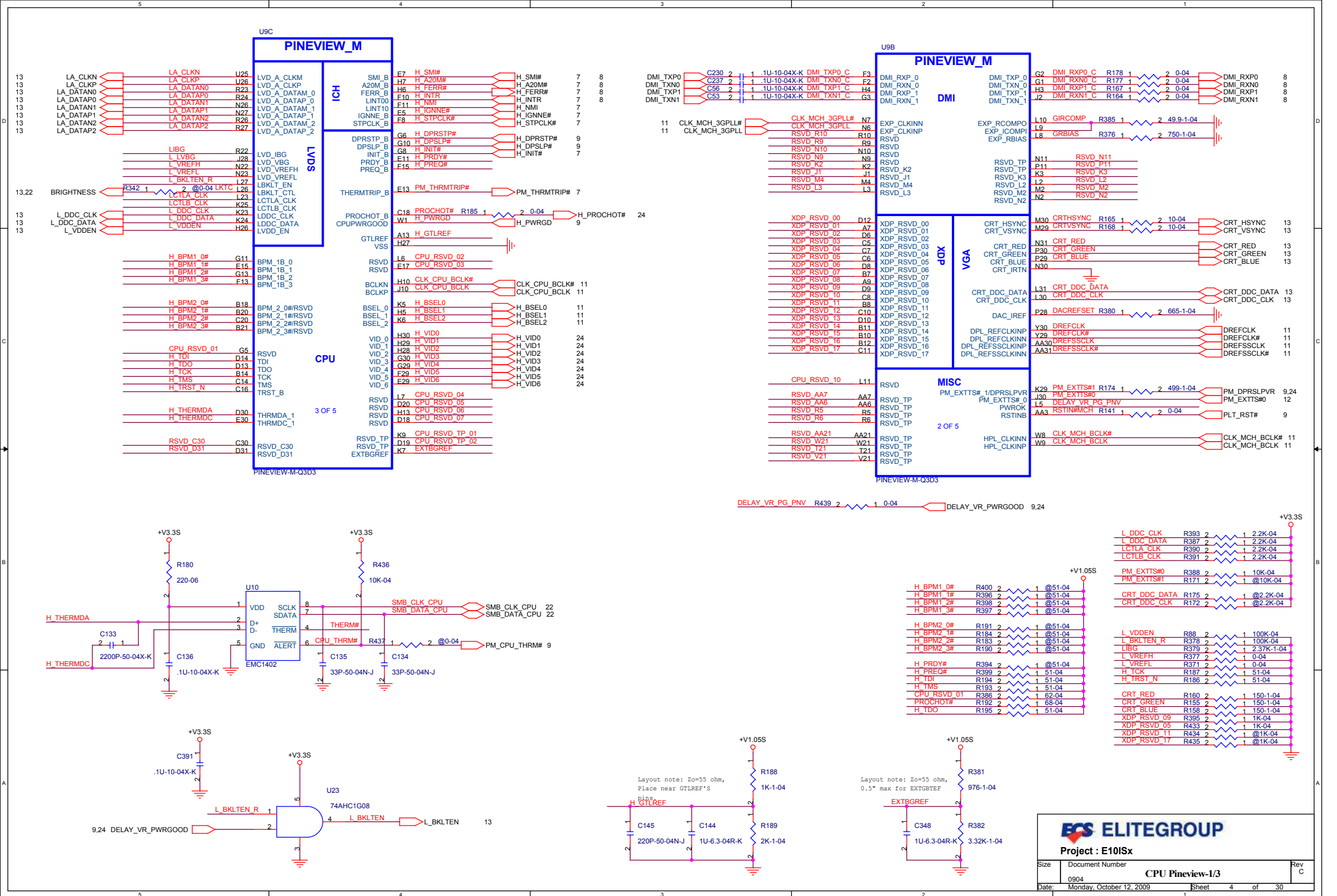
E10ISx Power on Sequence Diagram

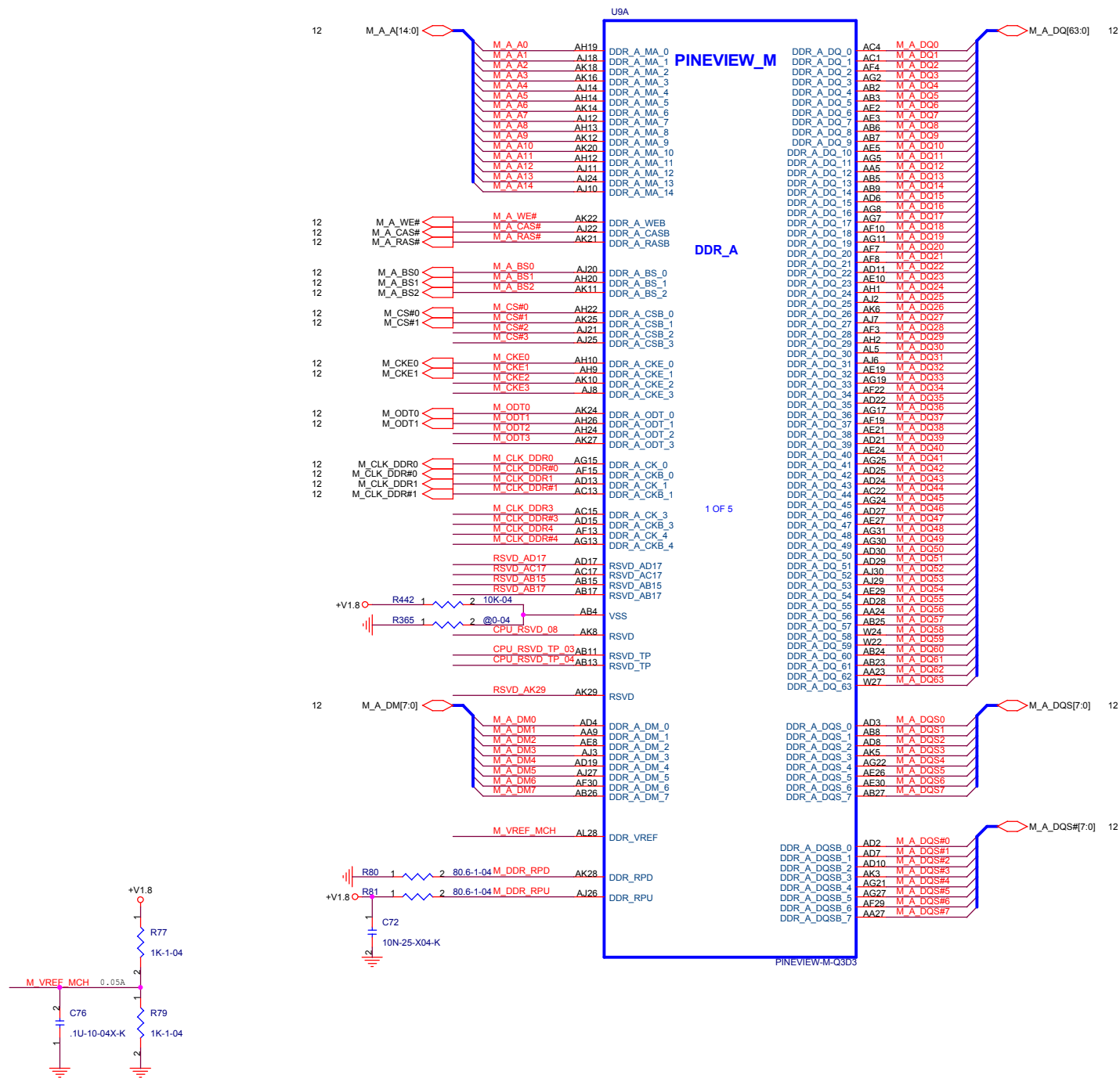


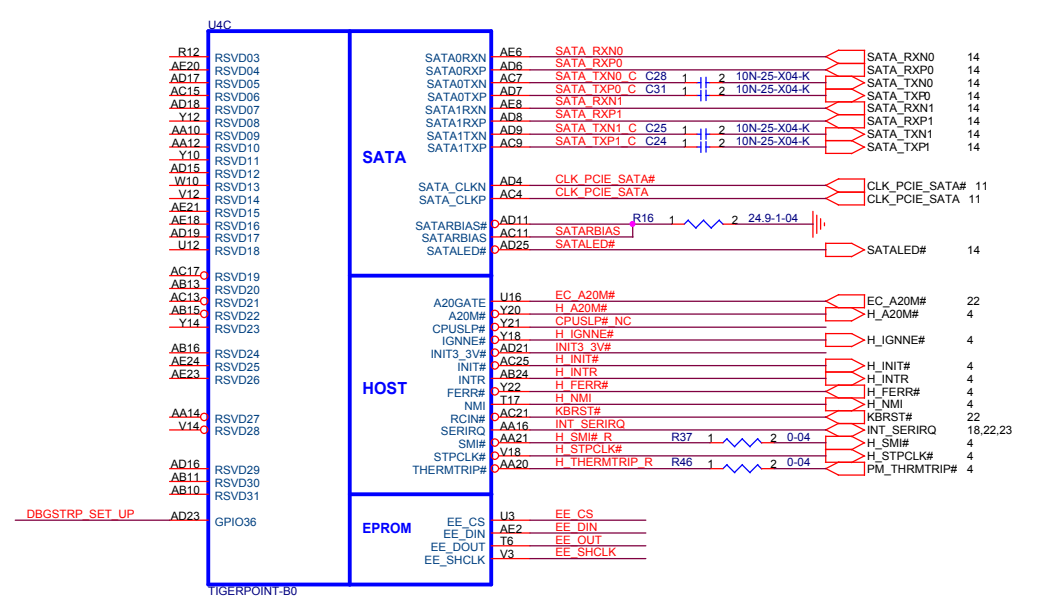
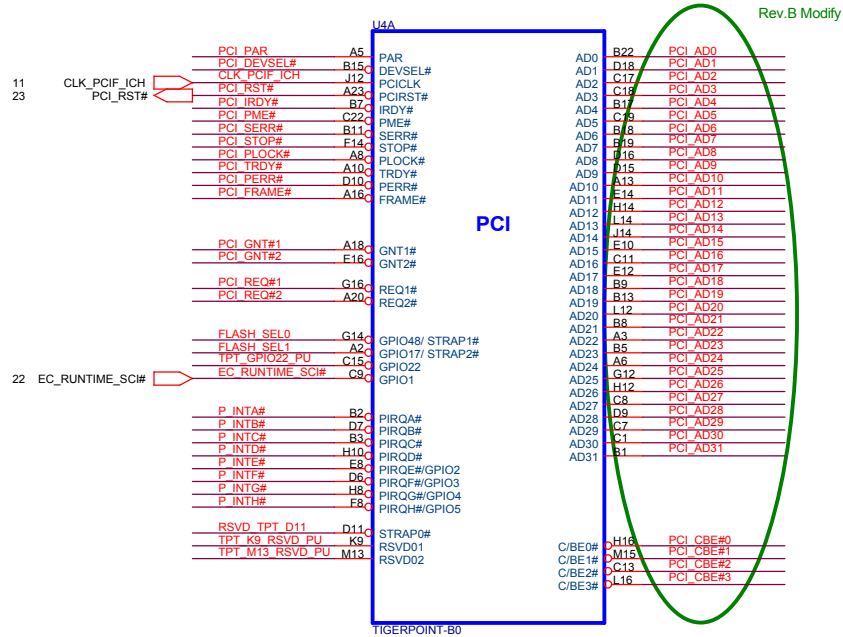
System Block Diagram

Clock delivery Block

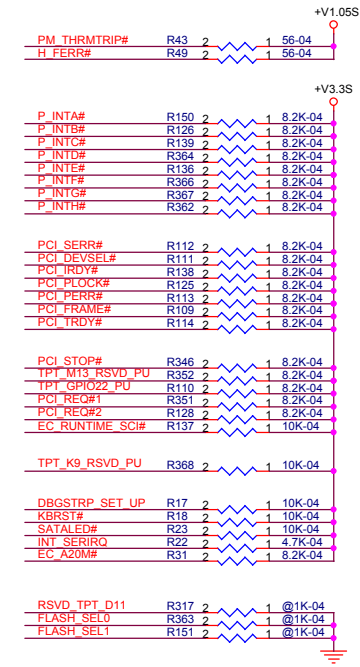


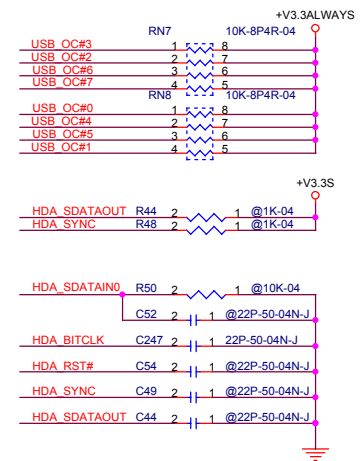
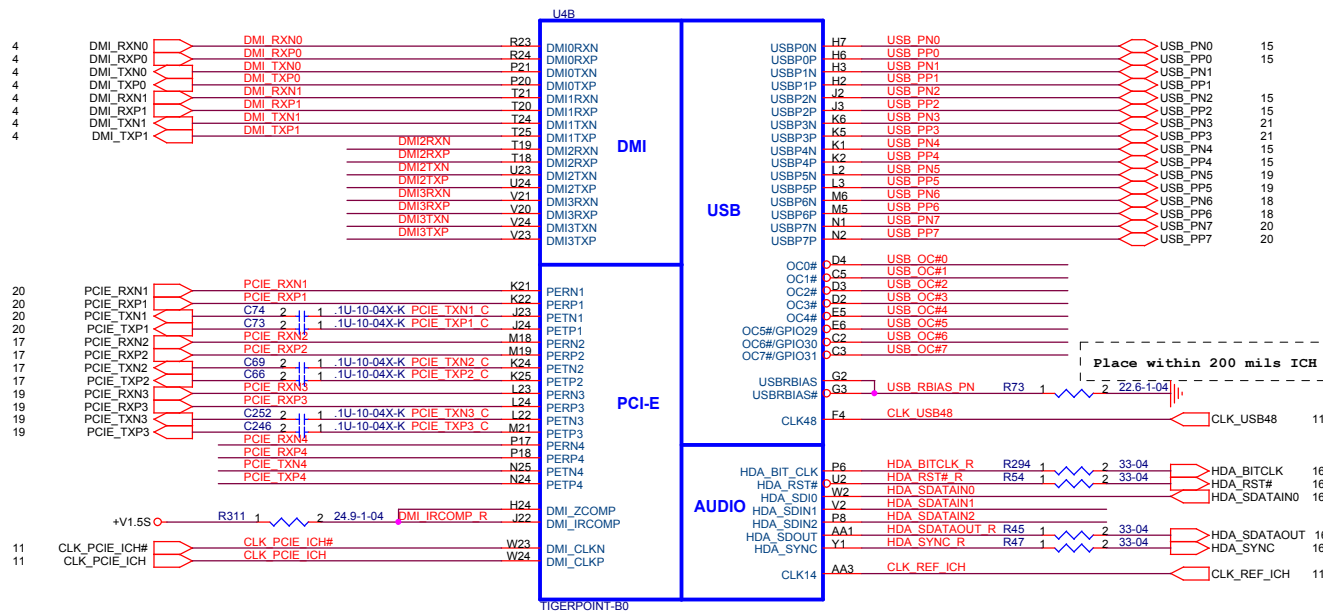




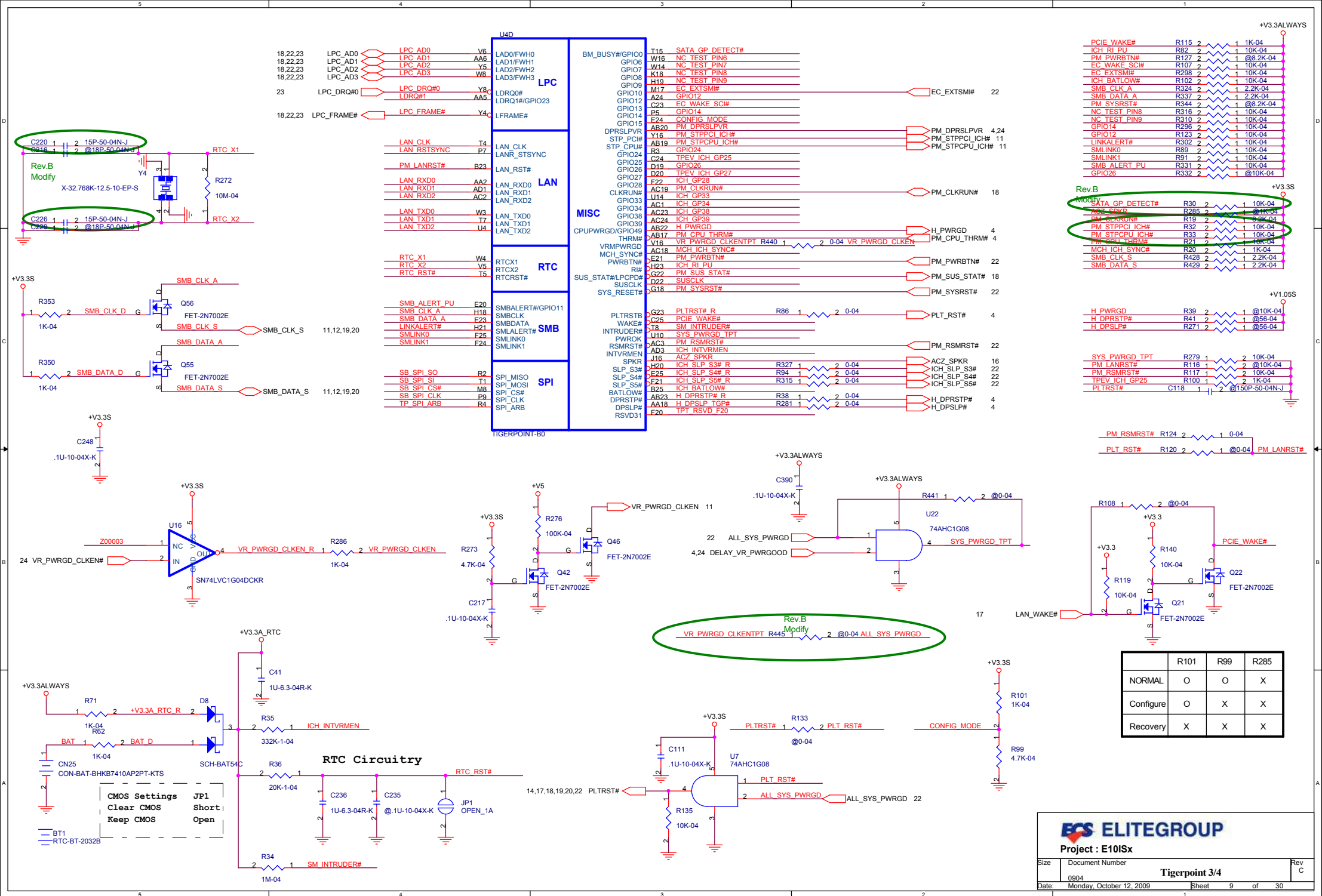


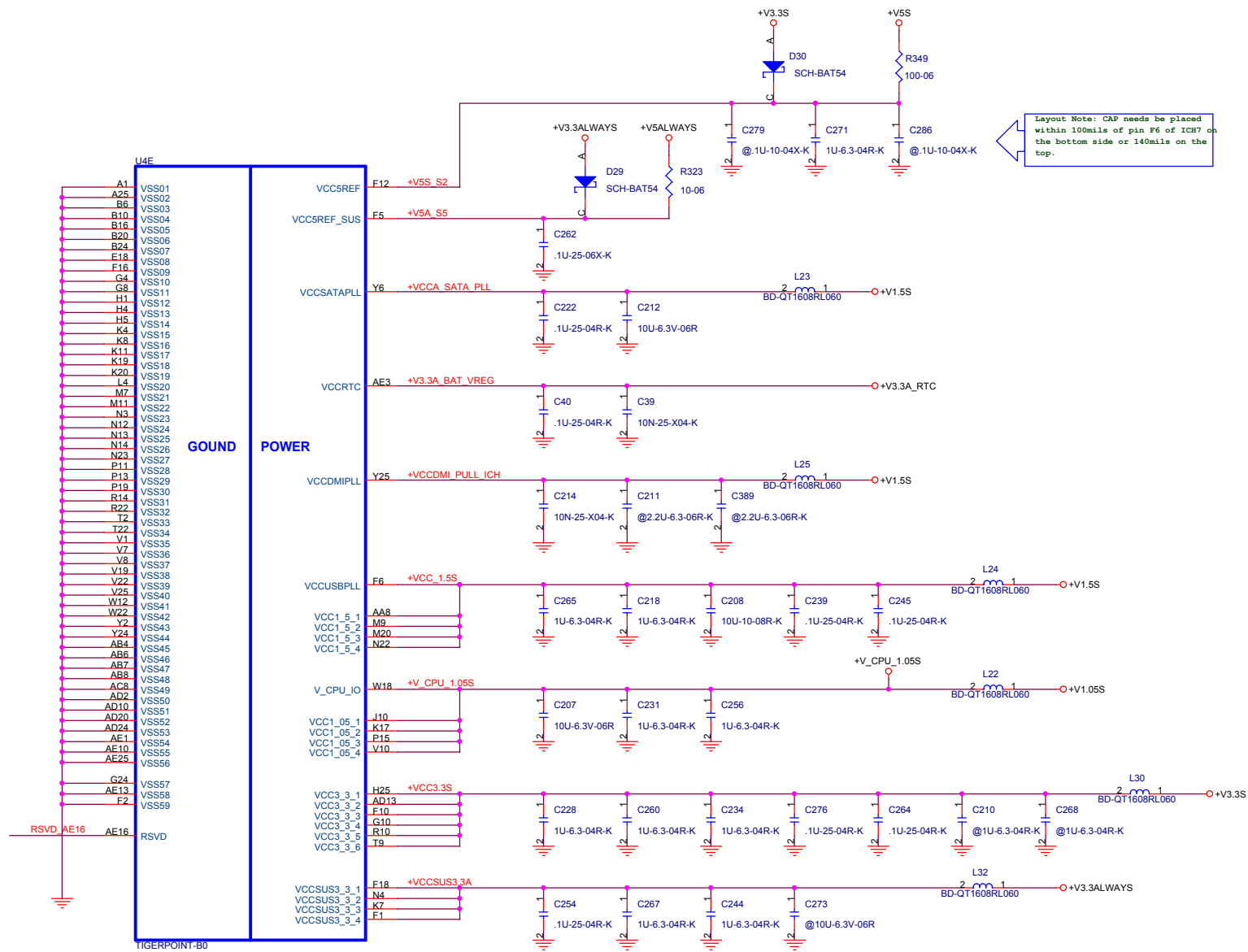
	FLASH_SEL	FLASH_SEL
SPI	1	0
PCI	0	1
LPC	1	1

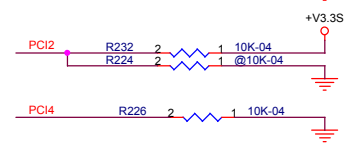
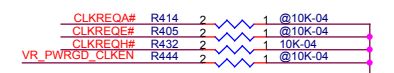
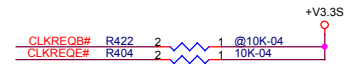
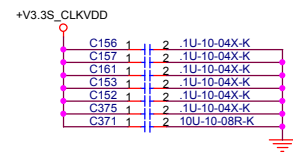
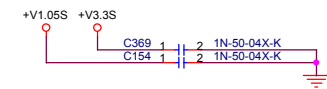
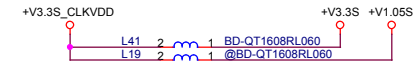




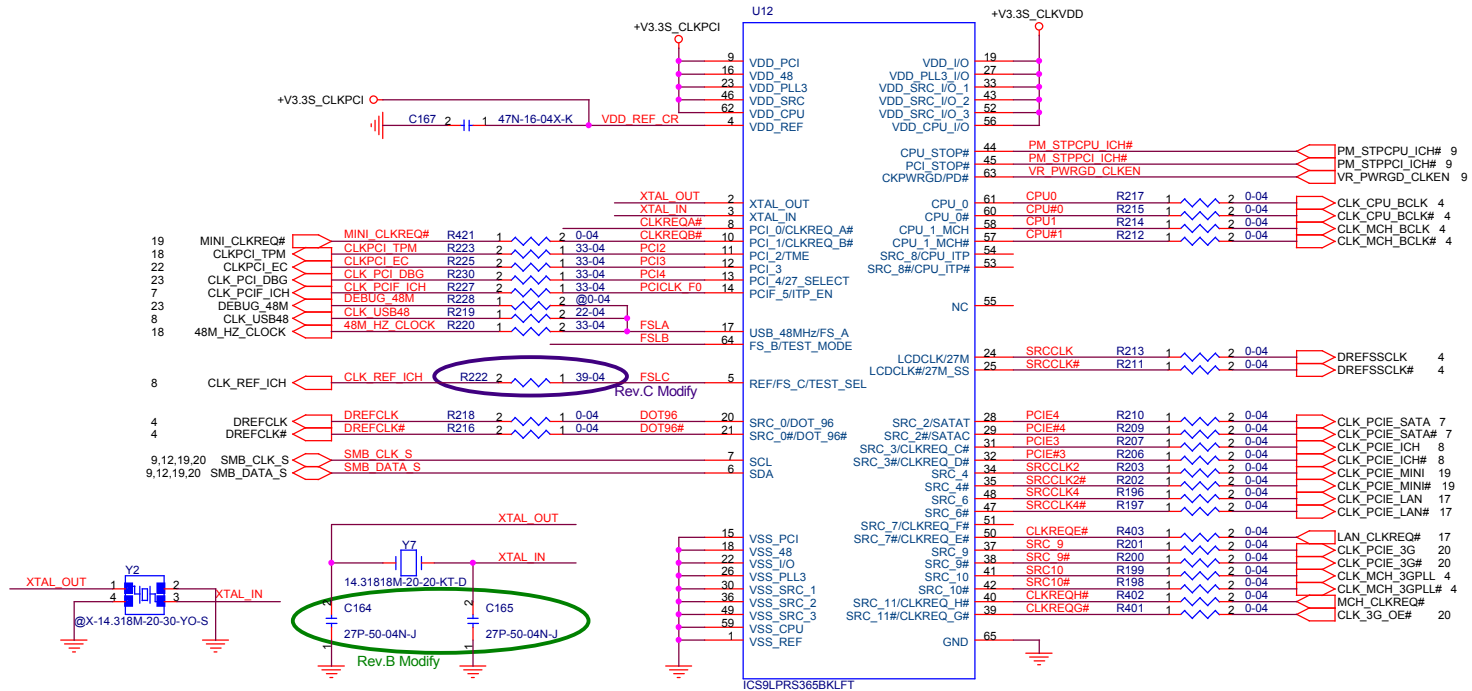
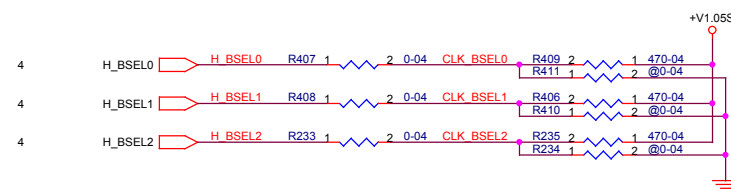
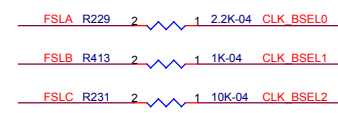
HDA_SDATAOUT	PCI Express port config bit 1
HDA_SYNC	PCI Express port config bit 0



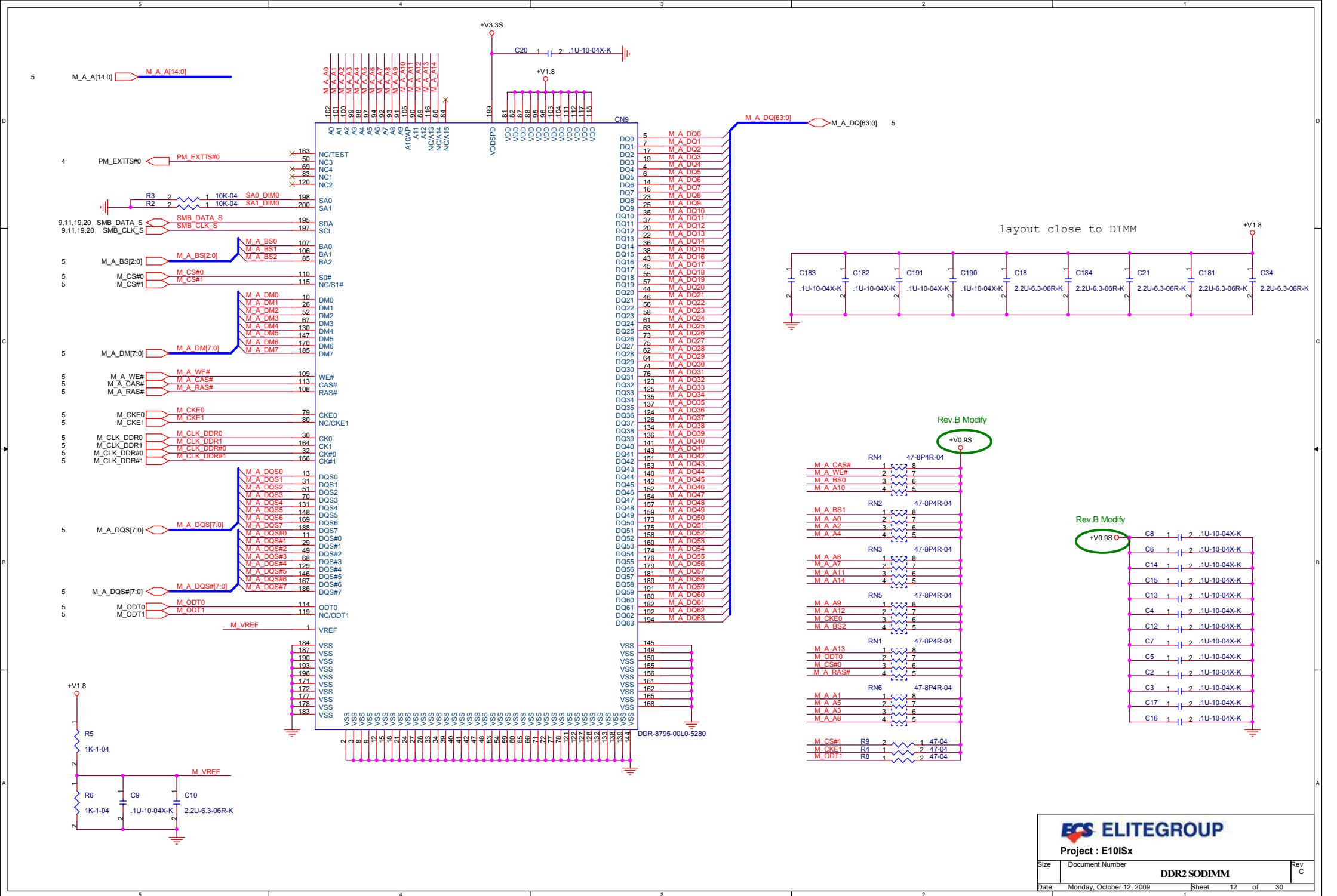




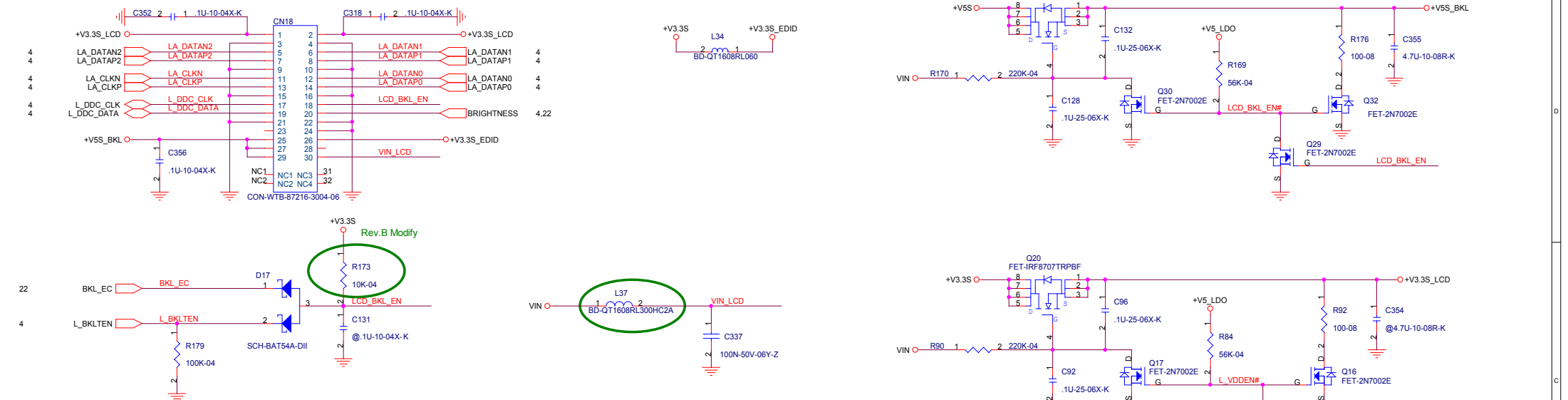
CLKPCI TPM	C169	2	1	@10P-25-04N
CLKPCI EC	C170	2	1	@10P-25-04N
CLK PCI DBG	C171	1	2	@10P-25-04N
CLK PCIF ICH	C168	2	1	@10P-25-04N
CLK USB48	C162	1	2	@10P-25-04N
48M HZ CLOCK	C163	1	2	@10P-25-04N
CLK REF ICH	C172	2	1	@10P-25-04N



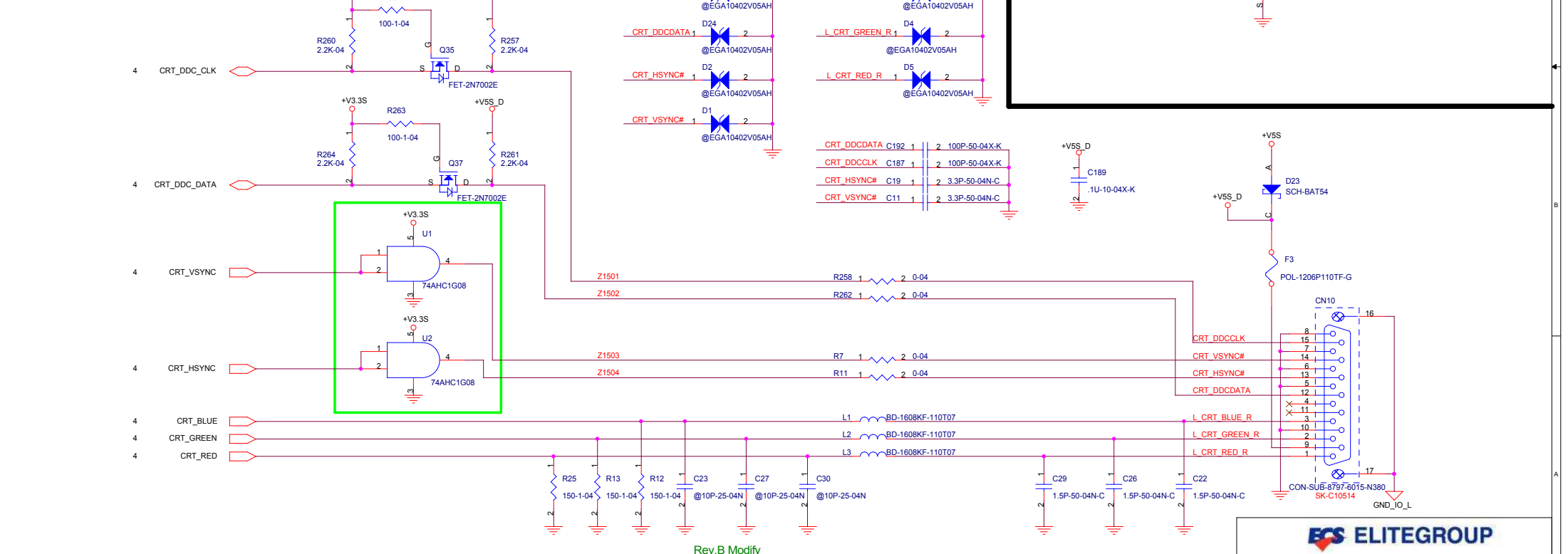
FSLC BSEL2	FSLB BSEL1	FSLA BSEL0	CPU	SRC	PCI
L	L	H	133MHz	100MHz	33MHz
L	H	L	200MHz	100MHz	33MHz
L	H	H	166MHz	100MHz	33MHz
H	L	H	100MHz	100MHz	33MHz
H	H	L	400MHz	100MHz	33MHz
H	L	L	333MHz	100MHz	33MHz
H	H	H	CPU SEL	100MHz	33MHz



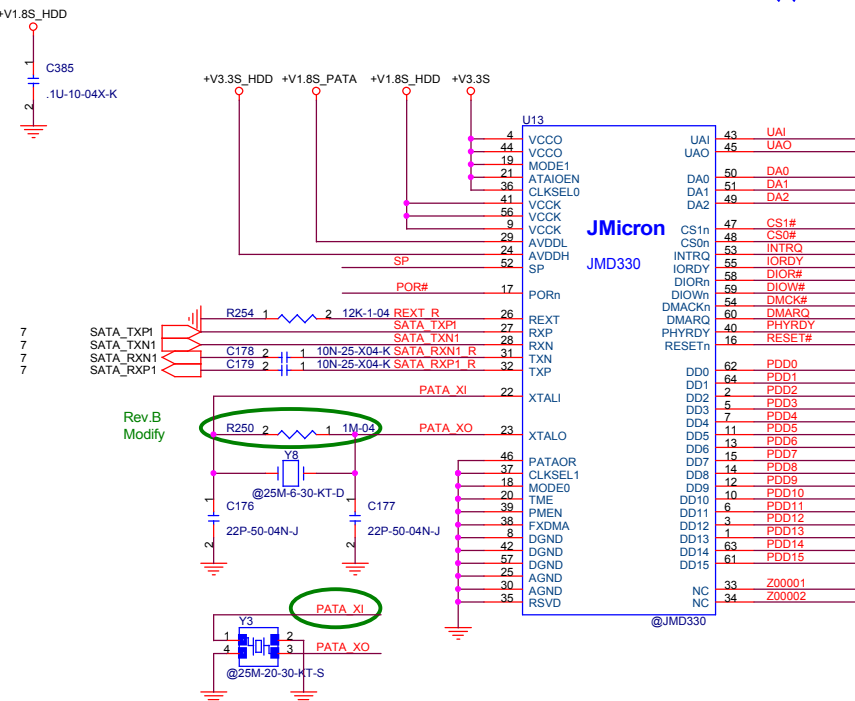
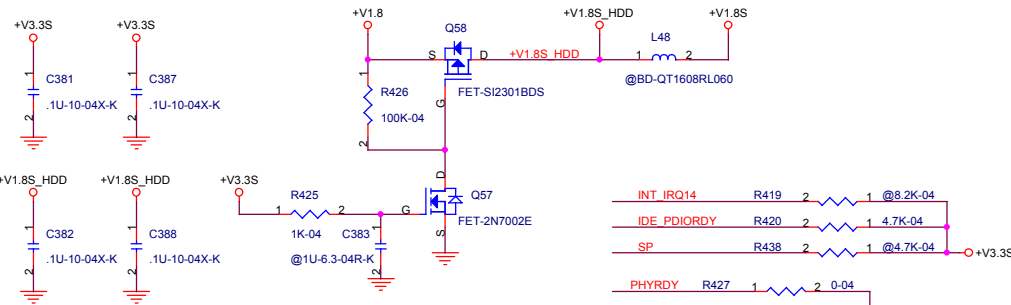
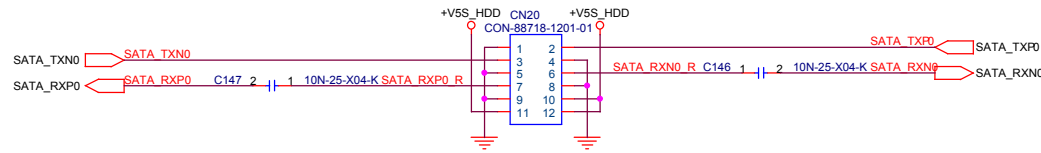
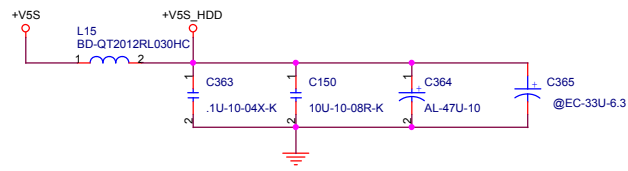
LCD



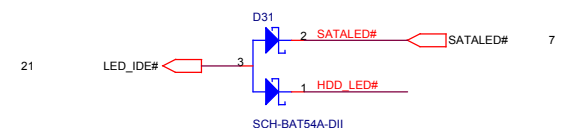
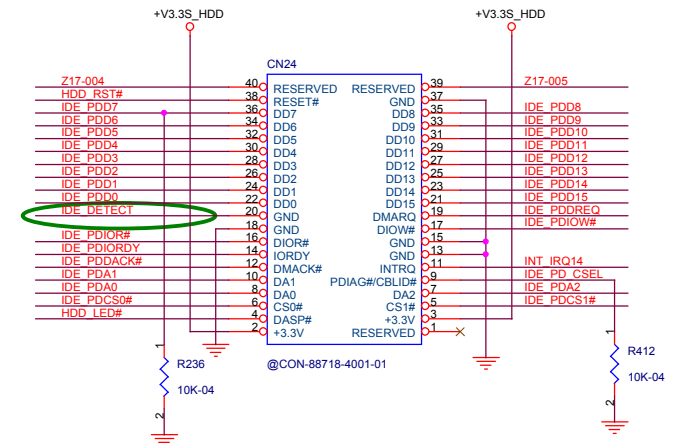
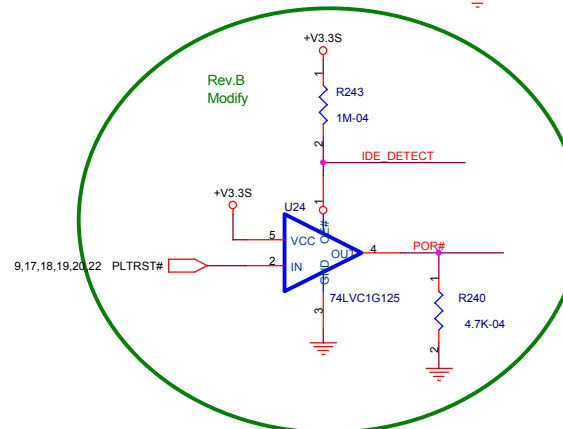
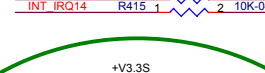
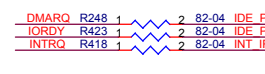
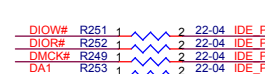
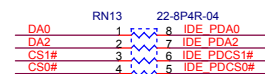
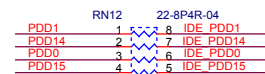
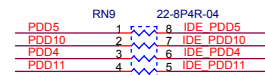
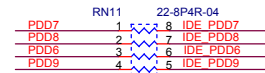
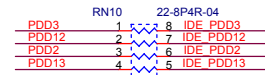
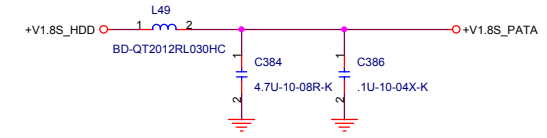
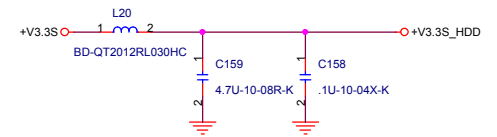
CRT



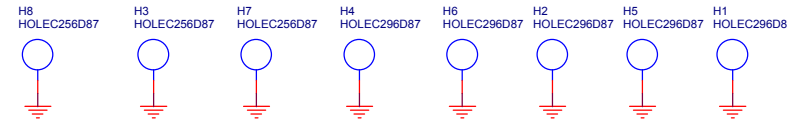
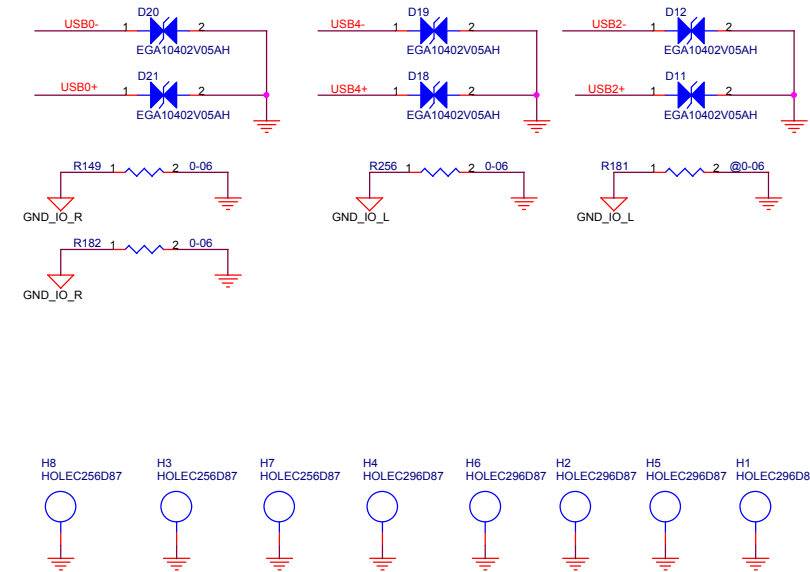
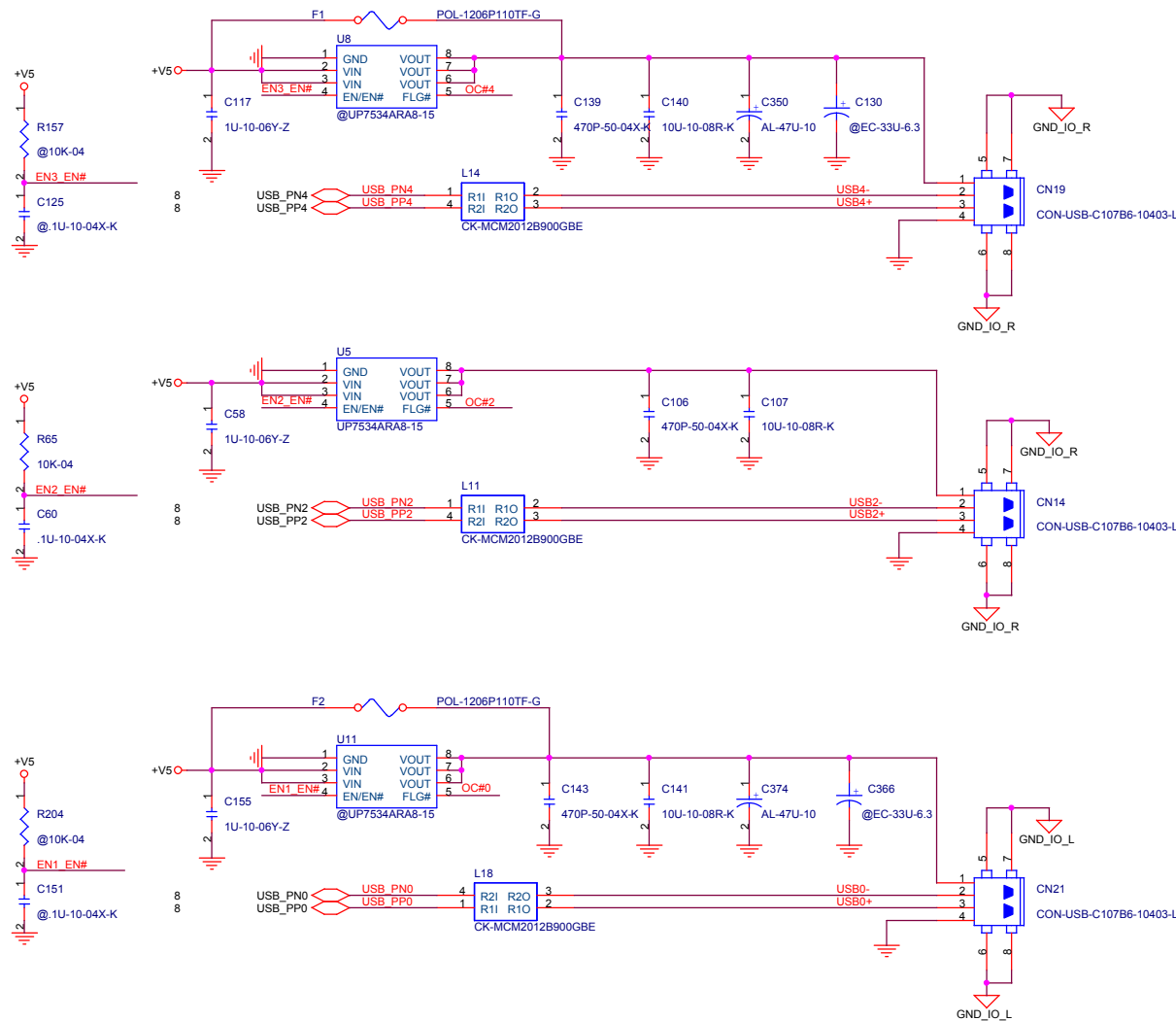
SATA HDD

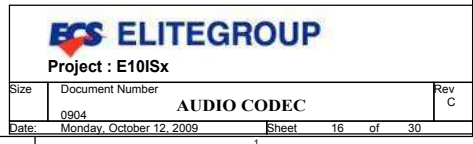


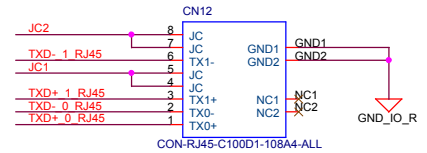
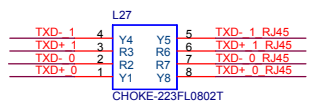
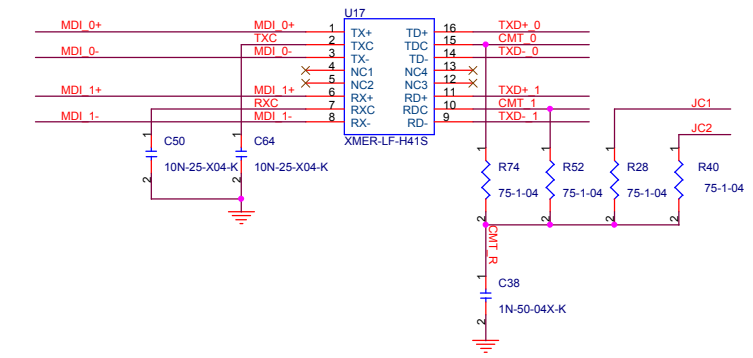
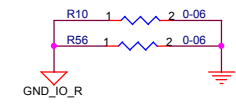
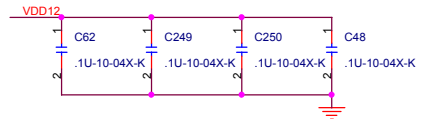
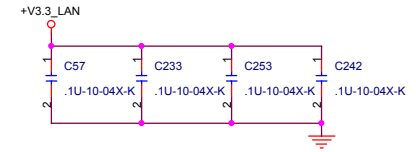
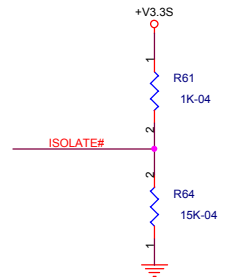
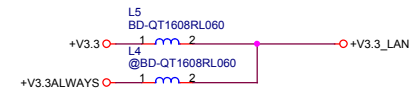
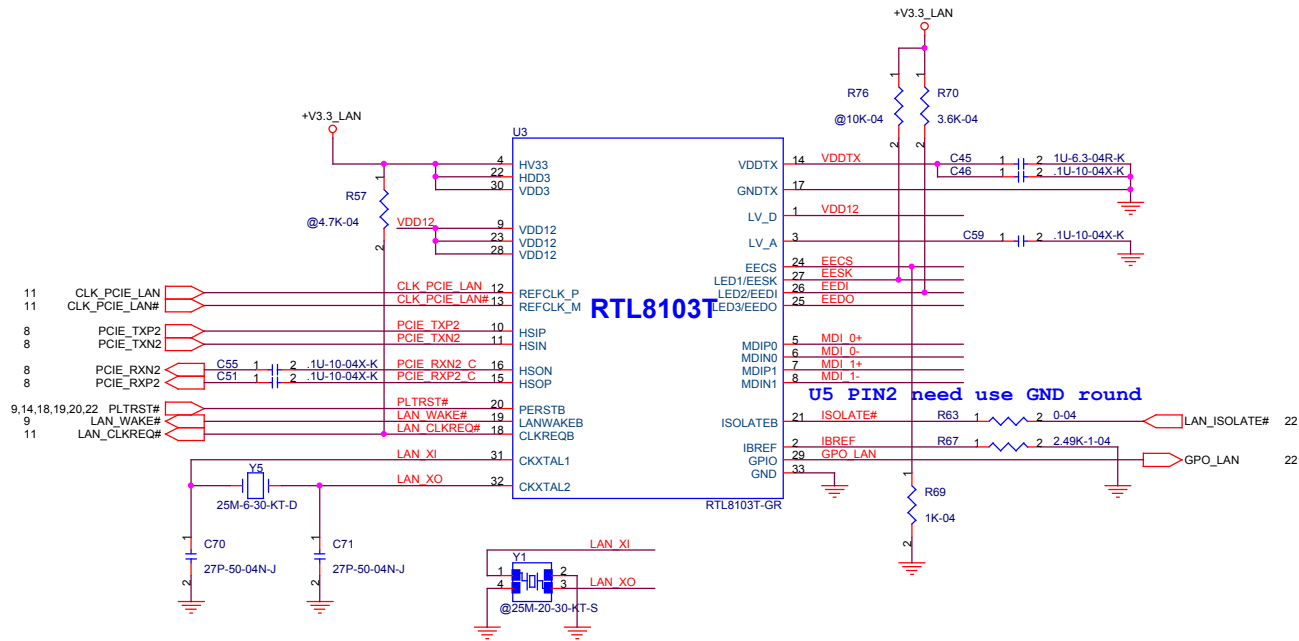
PATA Flash



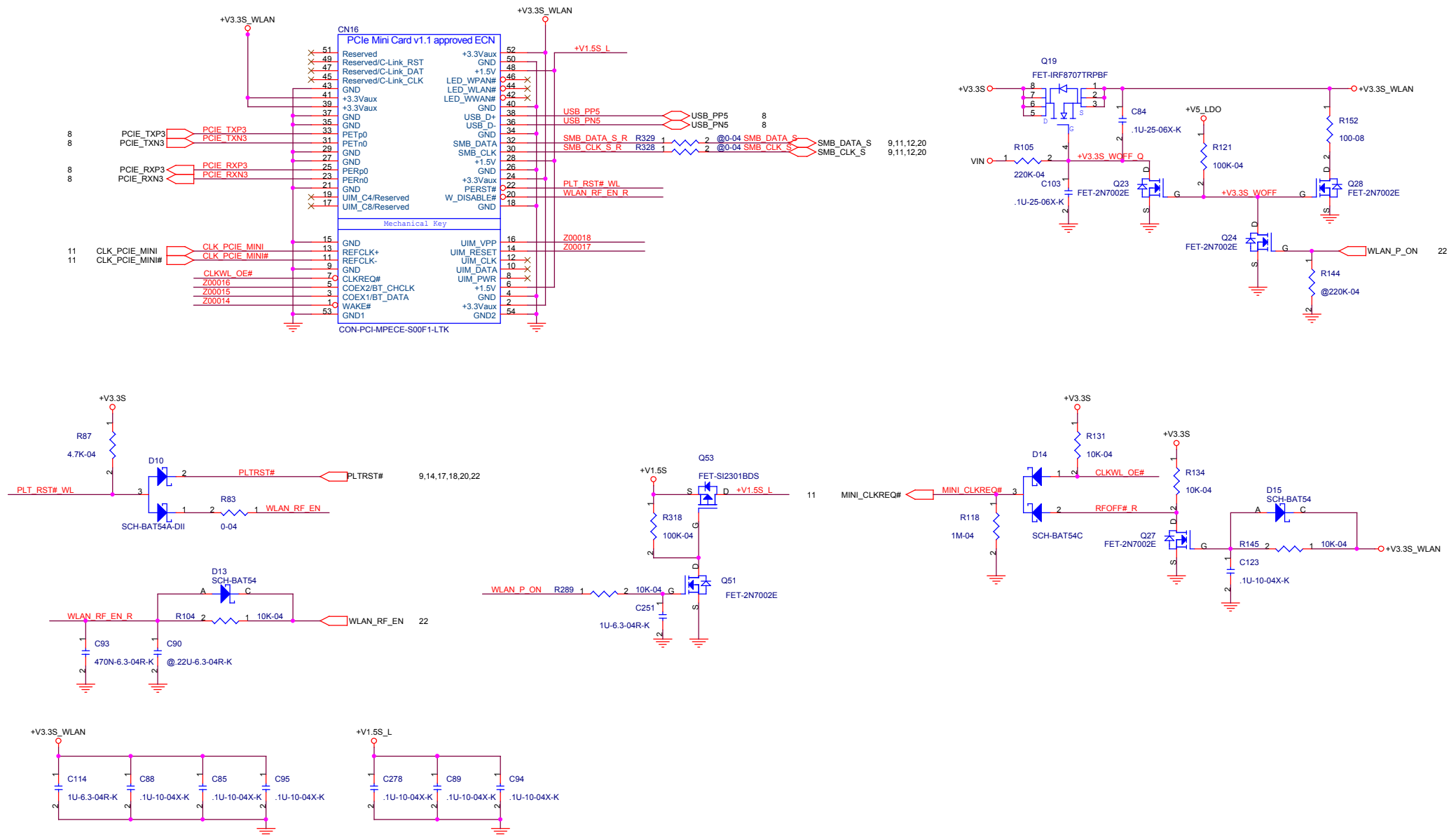
ESD protect



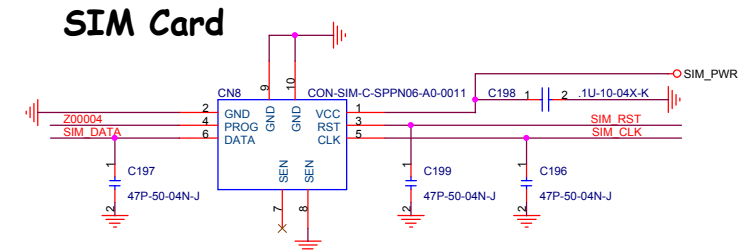
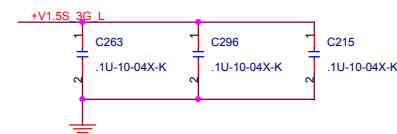
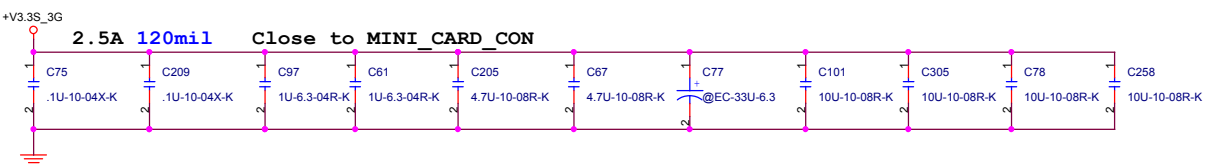
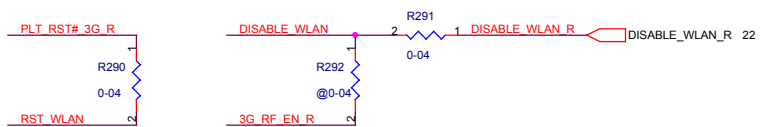
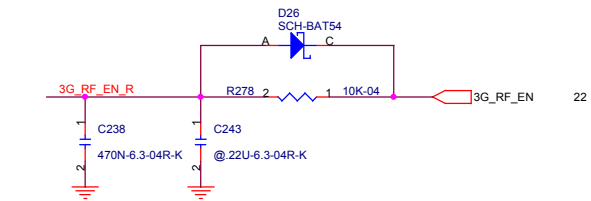
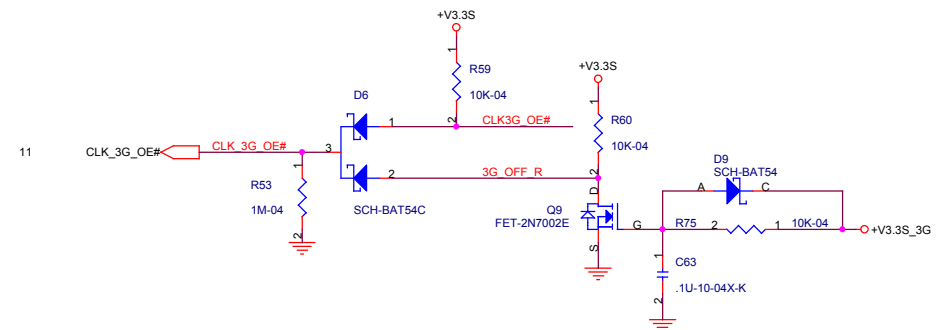
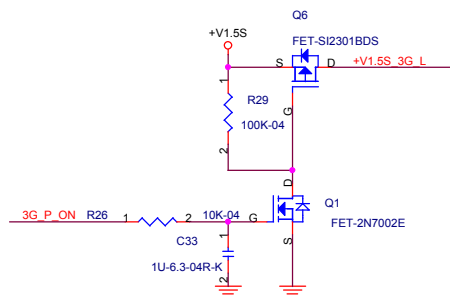
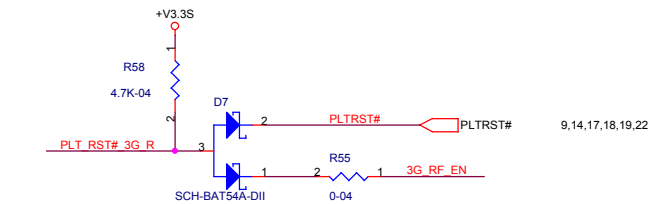
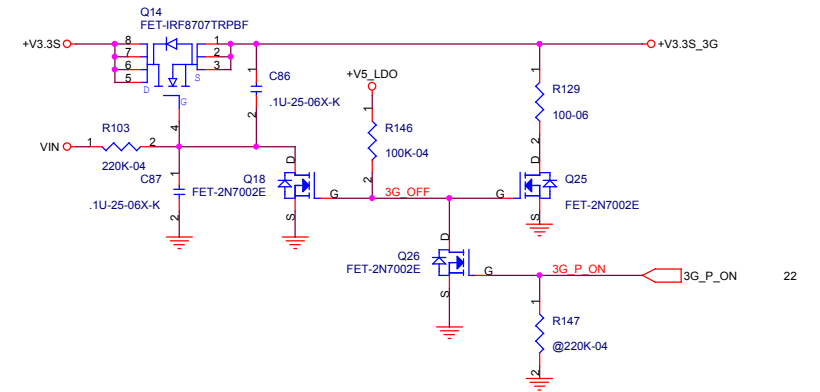
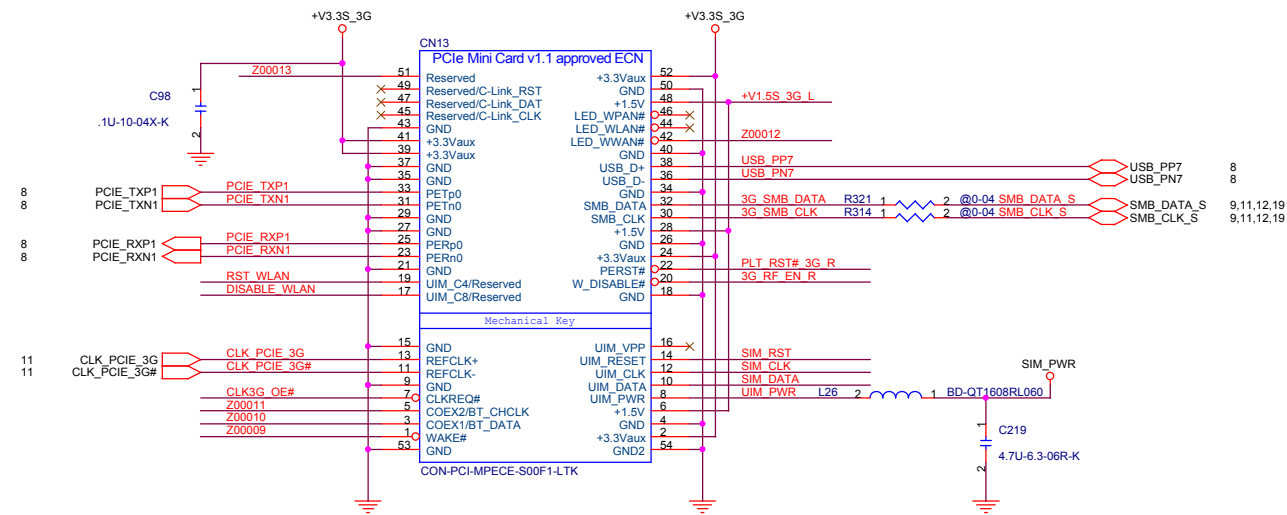




Mini Card



Mini Card_3G

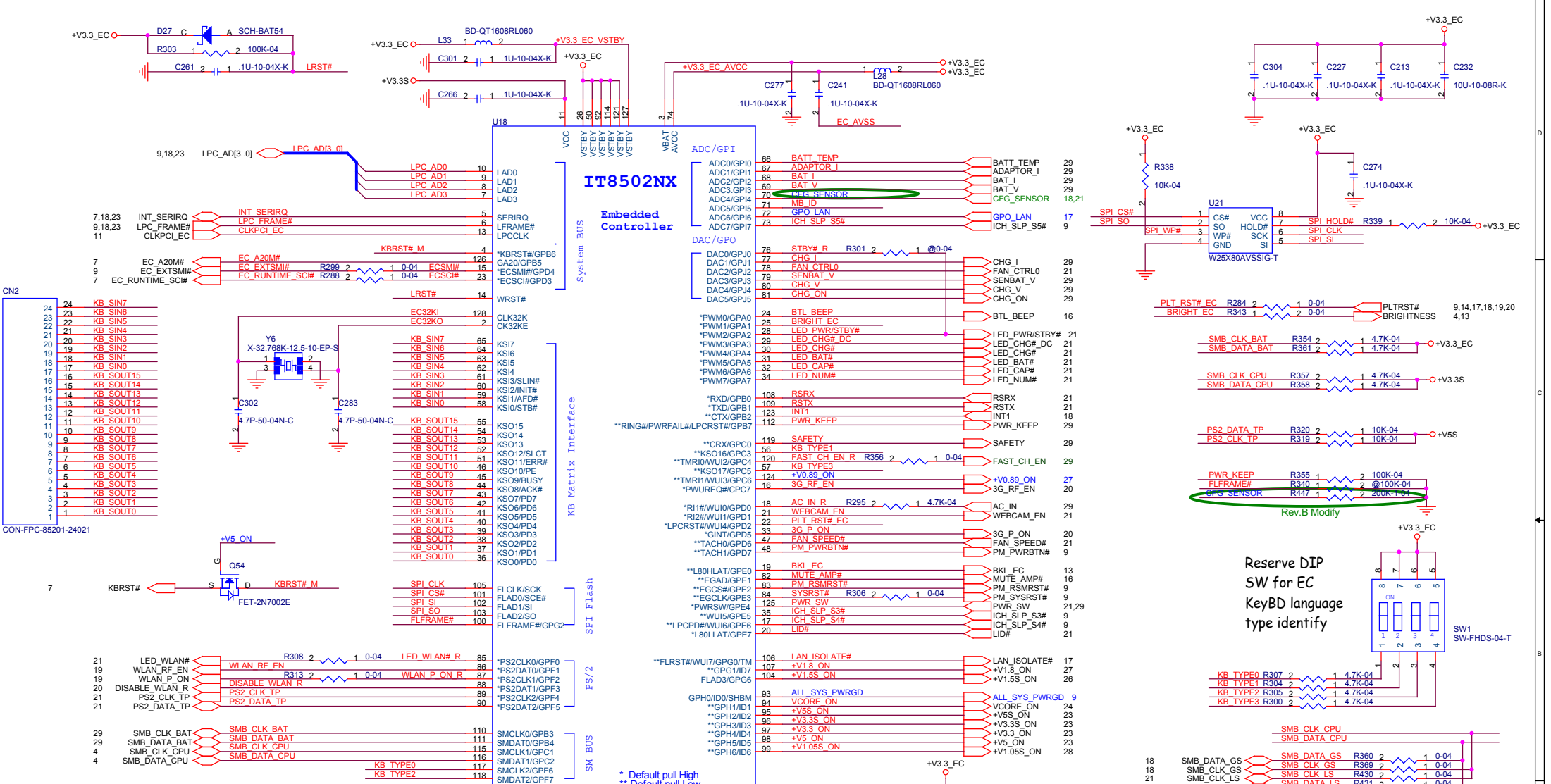


SIM Card

ECS ELITEGROUP

Project : E10ISx

Size	Document Number	Rev
	0904 3G	C
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KeyBD language type select Table

DIP Switch	1	2	3	4
English	ON	ON	ON	ON
French	OFF	ON	ON	ON
German	ON	OFF	ON	ON
Hungarian	OFF	OFF	ON	ON
Reserved	ON	ON	OFF	OFF
Portuguese(Brazil)	OFF	ON	OFF	ON
Spanish(Mexican)	ON	OFF	OFF	ON
Turkish,Greek,Italian	OFF	OFF	OFF	ON
Korean	ON	ON	OFF	ON

M/B revision ID

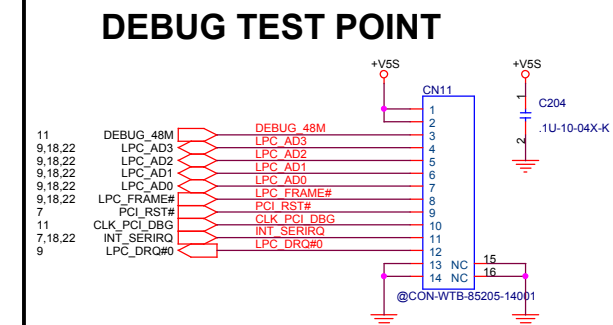
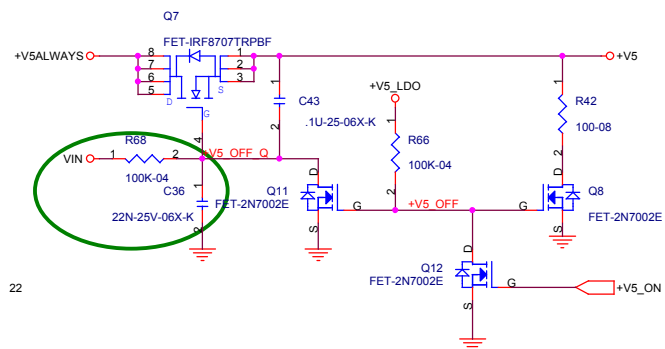
MB rev. ID	Rev.A	Rev.B	Rev.B W/O TPM	Rev.C	Rev.C W/O TPM
MB_ID	0V	0.5V	1V	3.3V	1.65V
R280	@	10K	10K	10K	10K
R283	1.5K	1.8K	4.3K	@	10K

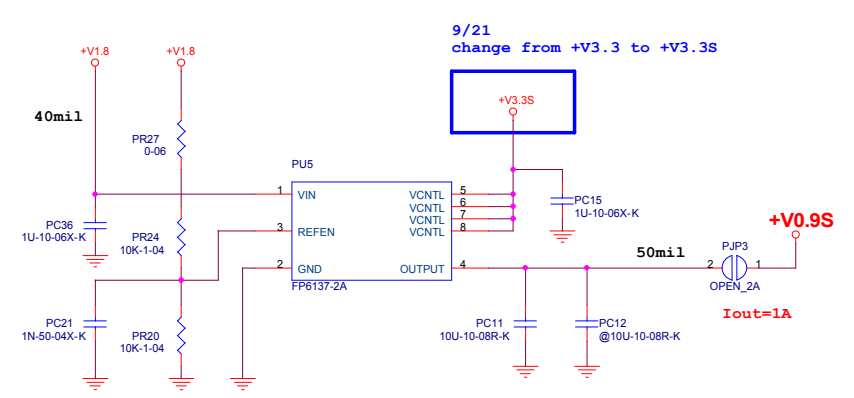
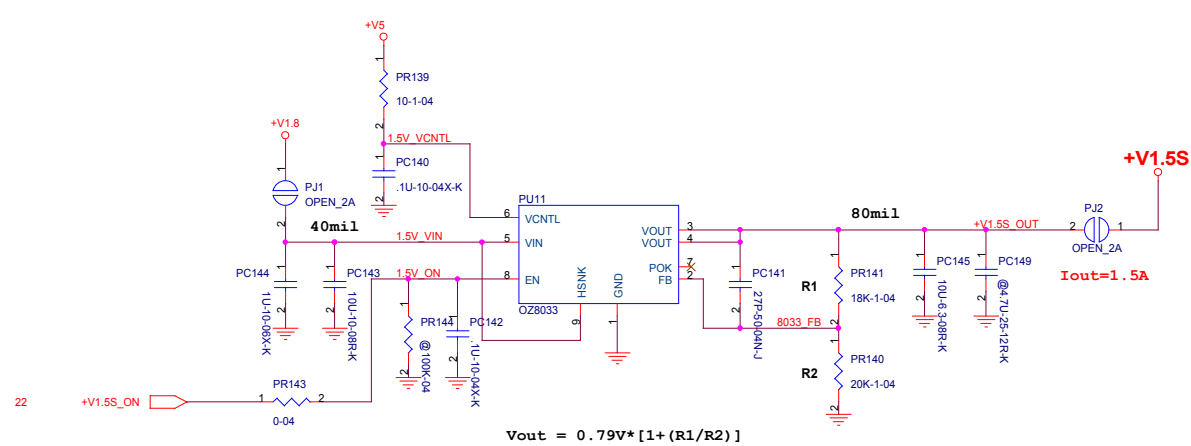
Configure Sensor

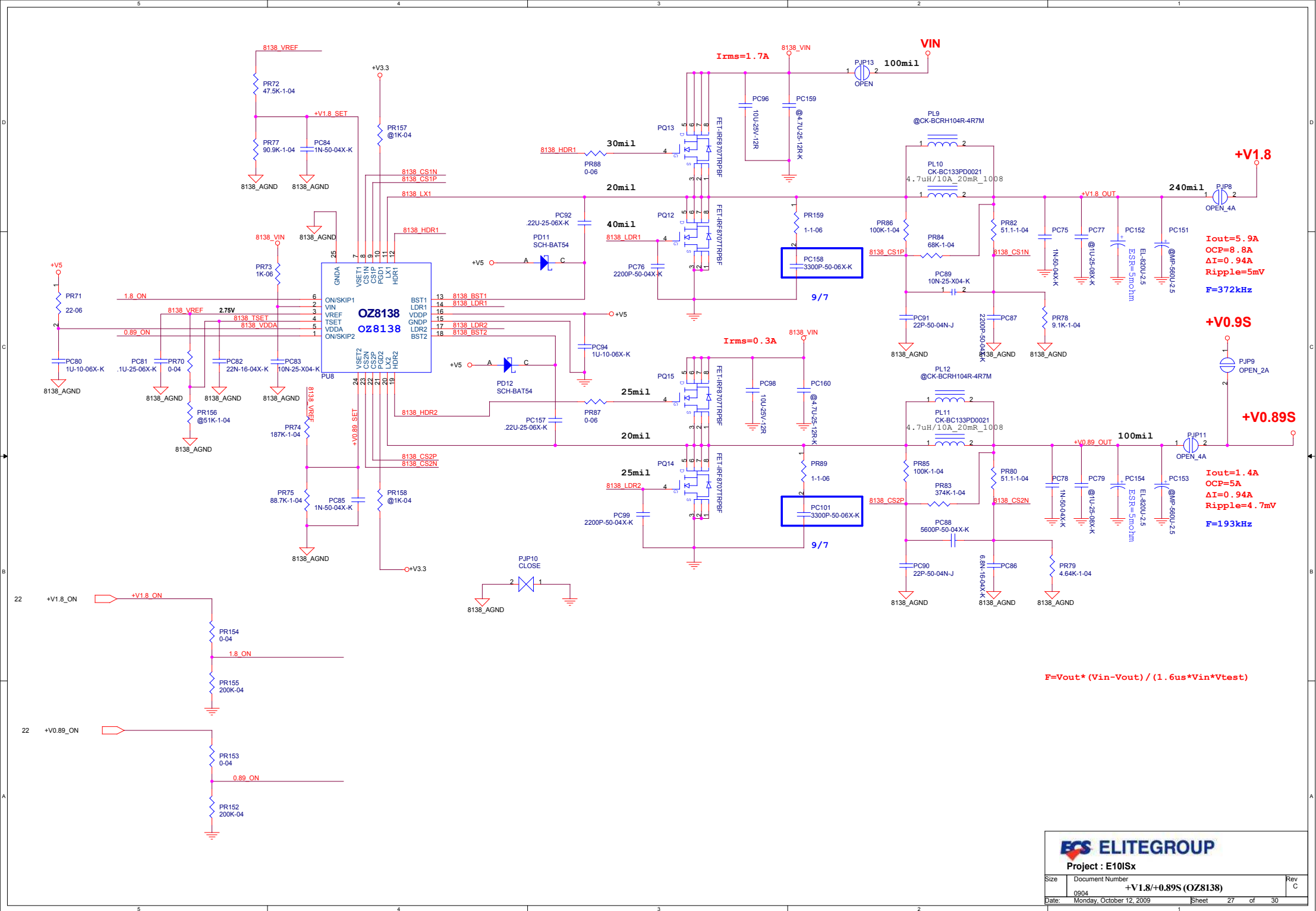
Configure	W/O Light Sensor & G-Sensor	Light Sensor Only	Light Sensor + G-Sensor	G-Sensor Only
Voltage	0 V	0.825 V	1.65 V	2.2 V

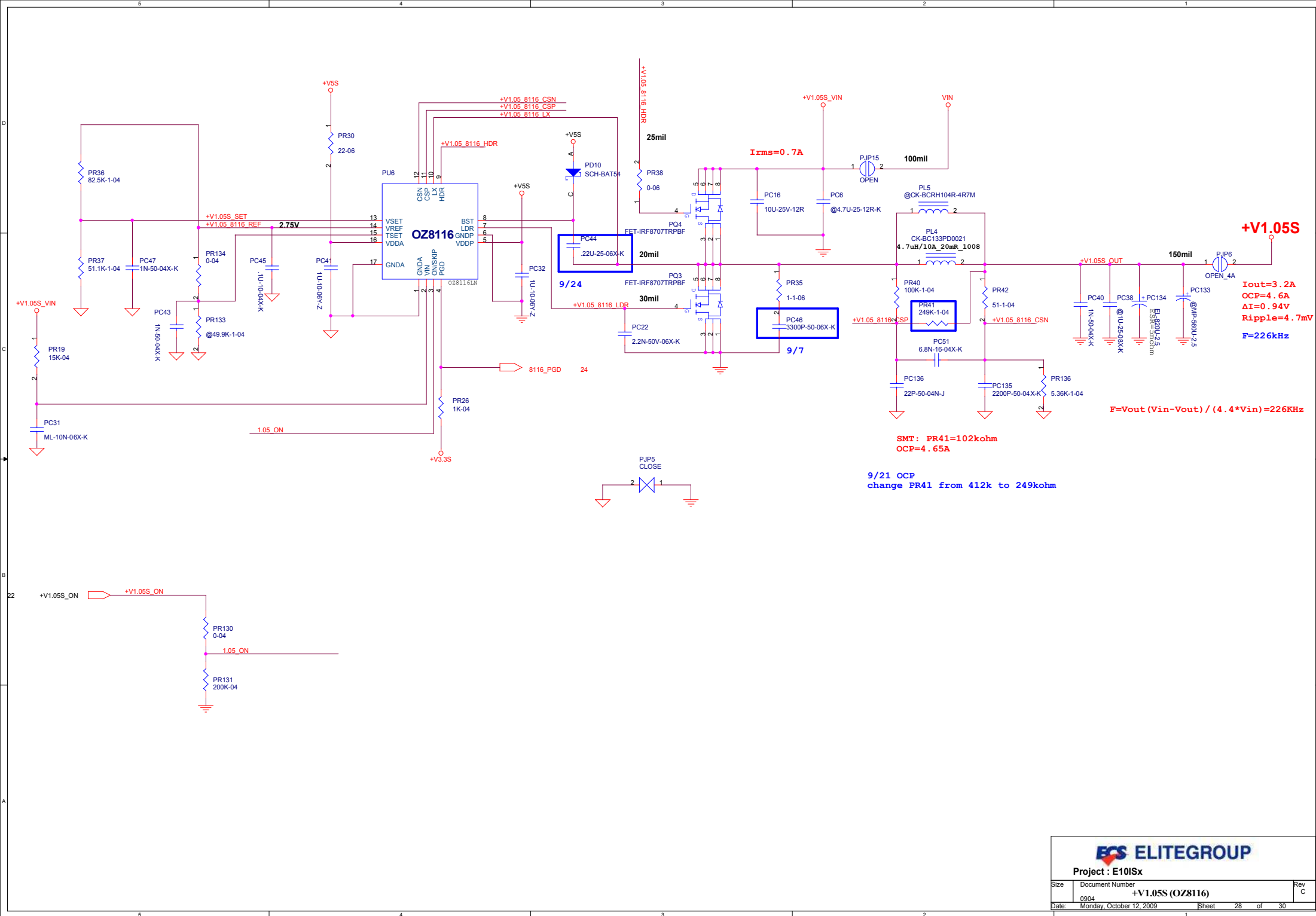
ELITEGROUP
Project : E10ISx

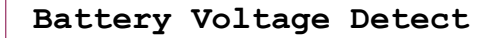
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E10ISx LED BD

PCB P/N : 35G4E1020-B0

LED placement



WLAN	NUM	CAP	HDD	PWR/STBY	CHG/BAT
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